

## Emilio Baungarten

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### Publication Metrics

For manuscripts published from date range April 2021 - April 2026

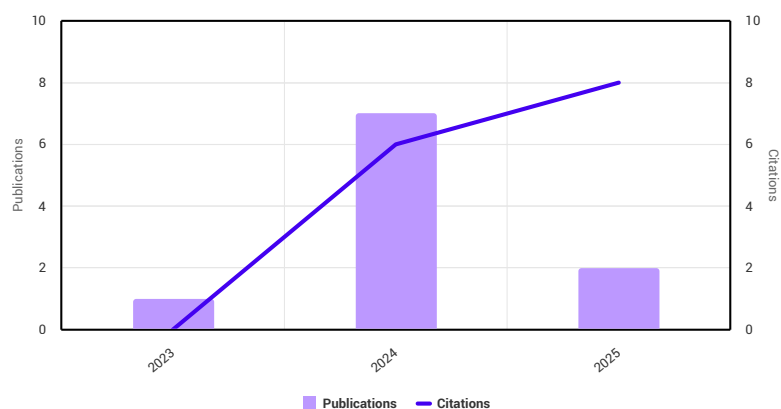
<b>3</b>	<b>19</b>
H-index	Sum of Times Cited
<b>16</b>	<b>12</b>
Total Publications	Web of Science Core Collection Publications

For all time

<b>3</b>	<b>19</b>
H-index	Sum of Times Cited
<b>16</b>	<b>12</b>
Total Publications	Web of Science Core Collection Publications

### Publication Impact Over Time

Times Cited and Publications Over Time



### Publishing Summary

For manuscripts published from date range April 2021 - April 2026

(5) Electronics

(2) Proceedings of the IEEE Internati...

(1) IEEE Embedded Systems Letters	(1) Zenodo
(1) Elements of Cloud Computing Se...	(1) International Conference on Elec...
(1) Applied Sciences	(1) Proceedings of the IEEE Internati...
(1) Proceedings of the IEEE Latin A...	(1) IEEE Access

## Publications

For manuscripts published from date range April 2021 - April 2026 (16) Times Cited  
(All time)

From RTL to Fabrication: Survey of Open-Source EDA Tools and PDKs 0  
 Published: Mar 2026 in Electronics  
 DOI: 10.3390/ELECTRONICS15051048

Edge Detection on a 2D-Mesh NoC with Systolic Arrays: From FPGA Validation to GDSII Proof-of-Concept 1  
 Published: Jan 2026 in Applied Sciences  
 DOI: 10.3390/APP16020702

Comprehensive RTL-to-GDSII Workflow for Custom Embedded FPGA Architectures Using Open-Source Tools 2  
 Published: Sep 2025 in Electronics  
 DOI: 10.3390/ELECTRONICS14193866

When AI Turns Malicious: Unethical Use of LLMs for Hardware Design 0  
 Published: 2025 in Proceedings of the IEEE International New Circuits and Systems Conference (NEWCAS)  
 DOI: 10.1109/NEWCAS64648.2025.11107096

Integrated Circuit Design Not indexed in  
the Web of  
Science  
 Published: 2025 in Elements of Cloud Computing Security  
 DOI: 10.1007/978-3-031-92108-7

On the Novel Design and FPGA Implementation of a Fuzzy PD Control for a DC Motor 0  
 Published: Oct 2024 in International Conference on Electrical Engineering, Computing Science and Automatic Control (CCE)  
 DOI: 10.1109/CCE62852.2024.10770929

Exploring New Approach With Open Source EDA Tools 0  
 Published: Sep 2024 in Zenodo  
 DOI: 10.5281/ZENODO.13245882

Automated IC Design Flow Using Open-Source Tools and 180 nm PDK 0  
 Published: Sep 2024 in Proceedings of the IEEE International Midwest Symposium on Circuits and Systems  
 DOI: 10.1109/MWSCAS60917.2024.10658750

<p>Design and Test of Offset Quadrature Phase-Shift Keying Modulator with GF180MCU Open Source Process Design Kit  Published: May 2024 in Electronics  DOI: 10.3390/ELECTRONICS13091705</p>	0
<p>The Genesis of AI by AI Integrated Circuit: Where AI Creates AI  Published: May 2024 in Electronics  DOI: 10.3390/ELECTRONICS13091704</p>	5
<p>Vector Accelerator Unit for Caravel  Published: Mar 2024 in IEEE Embedded Systems Letters  DOI: 10.1109/LES.2023.3267341</p>	5
<p>Transition from Synchronous to Asynchronous Systems with Minimal Logic Changes  Published: Feb 2024 in Proceedings of the IEEE Latin American Symposium on Circuits and Systems (LASCAS)  DOI: 10.1109/LASCAS60203.2024.10506182</p>	0
<p>RISC-Vcito: A Multicycle Tiny Processor Implemented with SKY130 PDK  Published: 2024 in Proceedings of the IEEE International Midwest Symposium on Circuits and Systems  DOI: 10.1109/MWSCAS60917.2024.10658692</p>	1
<p>Macro Memory Cell Generator for SKY130 PDK  Published: 2024 in IEEE Access  DOI: 10.1109/ACCESS.2024.3393479</p>	2
<p>Towards On-Board SAR Processing with FPGA Accelerators and a PCIe Interface  Published: Jun 2023 in Electronics  DOI: 10.3390/ELECTRONICS12122558</p>	3
<p>Extracción de modelos tridimensionales basado en estudios de angiografía  Published: Nov 2021</p>	<div data-bbox="1289 1429 1469 1529" style="background-color: #444; color: white; padding: 5px; text-align: center;"> Not indexed in  the Web of  Science </div>

## Verified Reviews

## Review Summary



## Reviewer Summary

For manuscripts reviewed from date range April 2021 - April 2026

(12) IEEE Access

### 12 REVIEWS OF 8 MANUSCRIPTS

For manuscripts published from date range April 2021 - April 2026

Low-Cost FPGA-Enhanced CNN Accelerator for Real-Time YOLO Object Detection & Classification

Reviewed: Nov 2025 for IEEE Access

FPGA-Based Image Processing Framework for Noise Reduction

Reviewed: Sep 2025 for IEEE Access

BAM-Net: Hardware Friendly Associative Memory Using Binary Dominant Weights and NAND Logic  
2 rounds from Jul 2025 to Aug 2025 for IEEE Access

Impact of Network Complexity on the Computational Performance of Ising Machines  
2 rounds from Apr 2025 to Jun 2025 for IEEE Access

Single-Node Power Demand During AI Training: Measurements on an 8-GPU NVIDIA H100 System  
Reviewed: Nov 2024 for IEEE Access

Better Scalability: Improvement of Block-based CNN Accelerator for FPGAs  
2 rounds from Oct 2024 to Nov 2024 for IEEE Access

Application Level Synthesis: Creating Matrix-Matrix Multiplication Library: A Case Study  
2 rounds from Aug 2024 to Oct 2024 for IEEE Access

A High-Level Synthesis Library for Synthesizing Efficient and Functional-Safe CNN Dataflow Accelerators  
Reviewed: Apr 2024 for IEEE Access

## Beamplot Summary

